FIG. 1

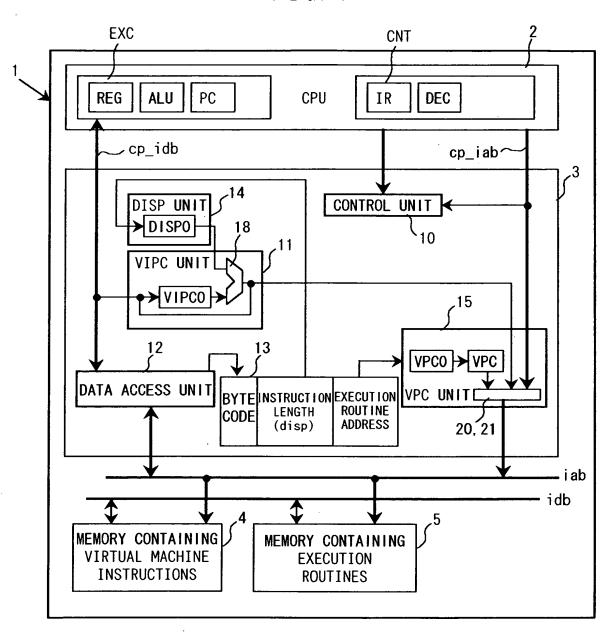


FIG. 2

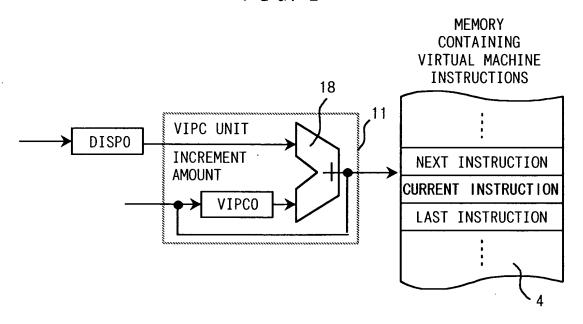
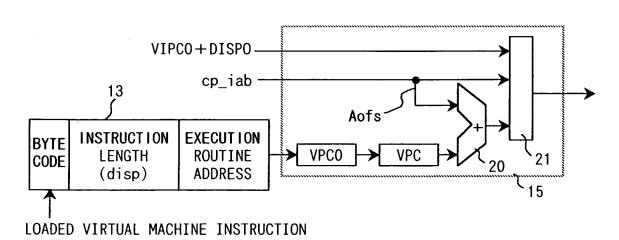


FIG. 3



## F I G. 4

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;VIPCO Write ;PRG\_TOP··· VIRTUAL MACHINE INSTRUCTION ADDRESS TO BE FIRST EXECUTED #H'PRG\_TOP, @VIPCO ;PRG\_TOP -> VIPC mov.w ;DISPO Write #H'00, @DISP0 ;H 0 -> DISPO mov.w ; VPC0 UPDATE COMMAND ISSUANCE ; LOAD VIRTUAL MACHINE INSTRUCTION ON VIPCO + DISPO, EXECUTION ROUTINE ; ADDRESS AND INSTRUCTION LENGTH, AND UPDATE VPCO imes DISPO #H'00, @VPC0chg MOV.W ; UPDATE COMMAND ; JUMP TO HEAD OF VIRTUAL MACHINE INSTRUCTION EXECUTION SPACE (H' 00210000) mov.l #H'00210000, R1 jmp @R1 ; JUMP TO HEAD OF VIRTUAL MACHINE INSTRUCTION EXECUTION SPACE

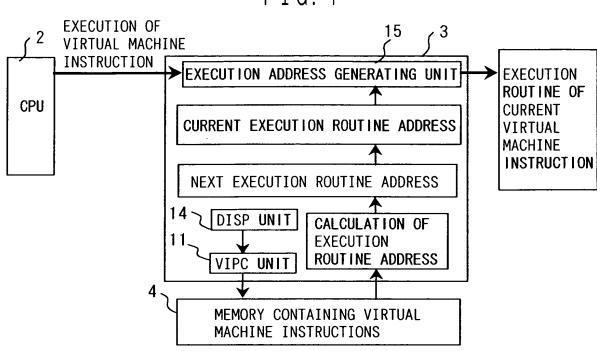
## FIG. 5

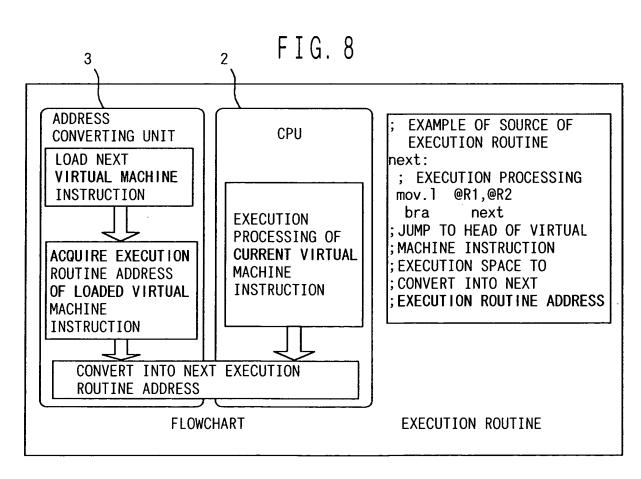
;EXAMPLE	OF EXEC	CUTION ROUT	INE (EXECUTING OPERATION [R1] -> [R2])
next:			; VIRTUAL MACHINE INSTRUCTION
			EXECUTION SPACE ADDRESS
mov.l	@R1,		; EXECUTION PROCESSING PORTION
bra	next		JUMP TO HEAD OF VIRTUAL MACHINE
			INSTRUCTION EXECUTION SPACE

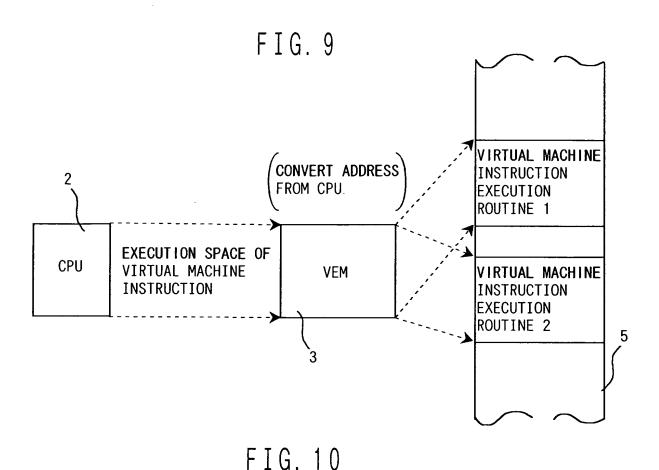
## FIG. 6

	ON ROUTINE OF VARI	ABLE-LENGTH INSTRUCTION AND BRANCH INSTRUCTION			
next:		; VIRTUAL MACHINE INSTRUCTION			
		EXECUTION SPACE ADDRESS			
	••••••	; EXECUTION PROCESSING PORTION			
***************************************					
mo∨.w	R1, @DISPO	; INSTRUCTION LENGTH OF VARIABLE-LENGTH			
1		; INSTRUCTION OR RELATIVE POSITION TO BRANCH			
		;DESTINATION FOR R1			
MOV.W	#00, @VPC0chg	; VPCO UPDATE COMMAND			
bra	next	;JUMP TO HEAD OF VIRTUAL MACHINE			
		INSTRUCTION EXECUTION SPACE			









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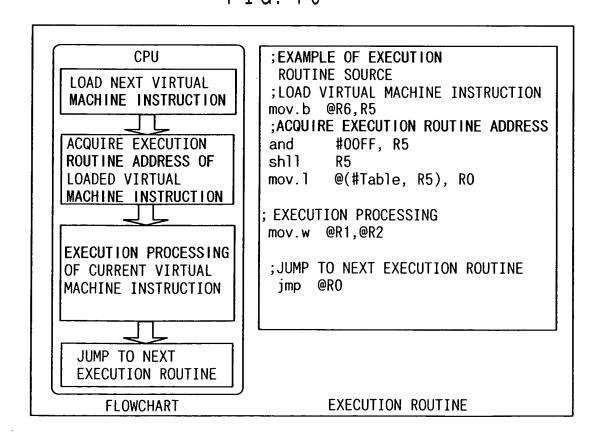


FIG. 11

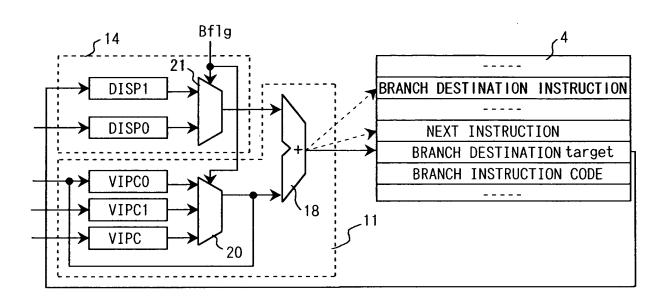
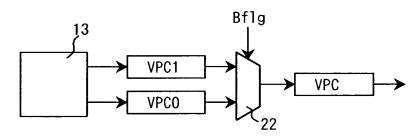


FIG. 12



## FIG. 13

next:

; VIRTUAL MACHINE INSTRUCTION

EXECUTION SPACE ADDRESS

mov.w @VIPCtoDISP1, R3

;[VIPC++] -> DISP1

mov.w #H'00, @VPC1chg

; VPC1 UPDATE COMMAND ; EXECUTION PROCESSING

1

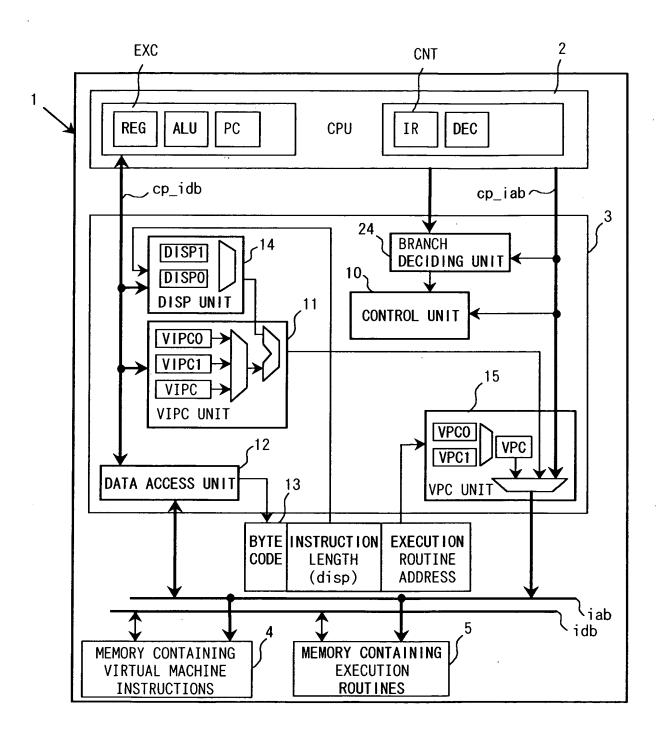
; JUMP TO HEAD OF VIRTUAL MACHINE

INSTRUCTION EXECUTION SPACE

bra

next

FIG. 14



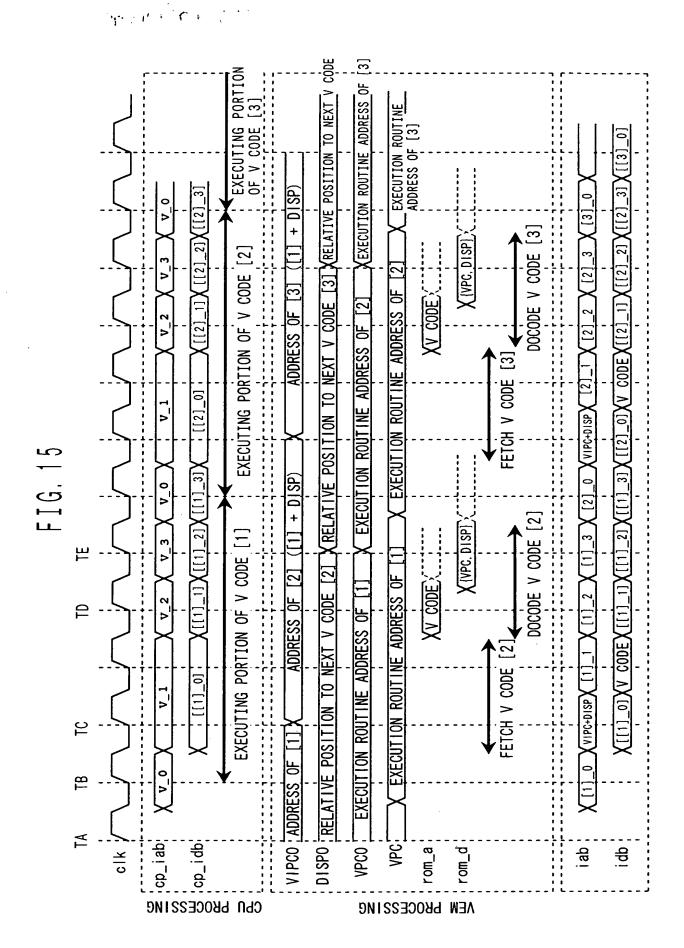
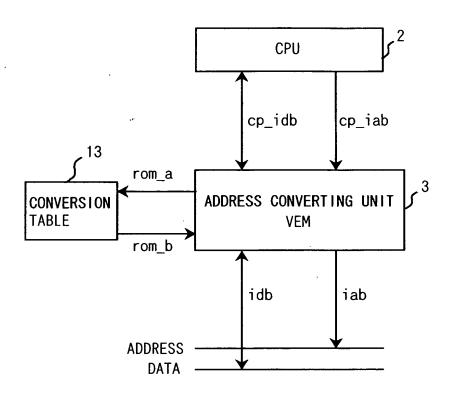


FIG. 16

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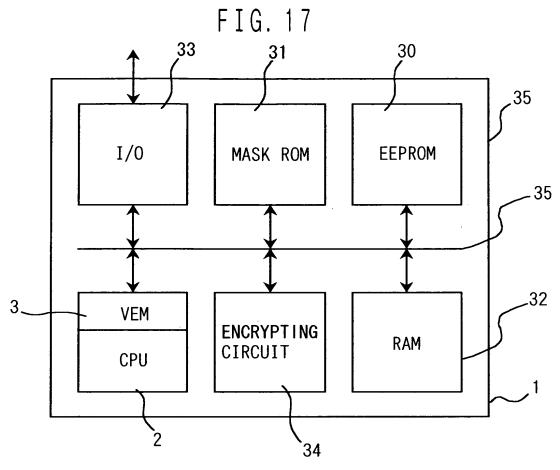


FIG. 18

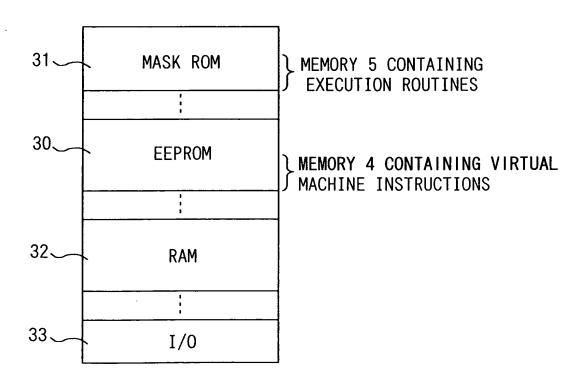


FIG. 19

1 1

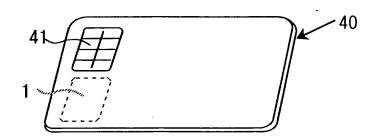


FIG. 20

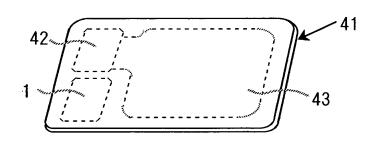


FIG. 21

